

Curriculum vitae

Personal Data

Name: Komarov Aleksey
Address: Uritskogo 125 - 132, 660017 Krasnoyarsk, Russia
Phone: +7 9831576492
Marital status: married
Nationality: Russian
Date of birth: 21. April 1990 in Osinniki, Russia

Education

2013-20016 PhD courses,
Dept. of Computer Engineering,
Institute of Space and Information Technologies,
Siberian Federal University, Krasnoyarsk, Russia
2007-2011 Bachelor in Computer science ,
Institute of Cybernetic Tomsk Polytechnic University
2011-2013 Master of microprocessor systems
Institute of space and informatic technologies Siberian Federal University
2000-2007 Secondary school, No. 47, Novokuznetsk, Russia
1997-2000 Elementary school, No. 90, Novokuznetsk, Russia

Research Interests Systems-on-Chip, Parallel Computational Systems,
Dataflow Computers, Functional Languages, Digital Signal
Processing, High-level Synthesis, field-programmable gate
array

Work Experience

2013- Engineer-programmer, Development Department,
Iskra R&D Corporation

2014-2015	Research-engineer, Research Laboratory of Microprocessor systems, Dept. of Computer Engineering, Institute of Space and Information Technologies, Siberian Federal University, Krasnoyarsk, Russia
2012 -	assistant of the Department of Computer Engineering, Institute of Space and Information Technologies, Siberian Federal University, Krasnoyarsk, Russia
Languages	Russian (native language) English (intermediate)
Computer skills	<u>Operating systems</u> : Linux, Windows <u>Programming language</u> : Verilog, C, C++,Qt Matlab scripting language, Shell, <u>Software/hardware development environment</u> : Xilinx ISE,Xilinx Vivado, Matlab Simulink, ISE System Generator for DSP,
Achievements	Bronze medal in 5th international exhibition of scientific- technical and innovative development "Dimension of the world, man 2015", Barnaul, 2015.
Certificates	The driver software for onboard the spacecraft (computer program), Certificate of state registration № 2015616896, 25.06.2015 Software for HDL synthesis from Pifagor programming Language, Certificate of state registration № 2015619175, 26.08.2015 Software of DVB-S2 signal receiver, Certificate of state

registration

№2014660589 2014.07.02.

Software of satellite modem YAR-1040 Certificate of state
Registration

№2015614726 12.01.2015

Xilinx Courses Designing with Verilog, Advanced VHDL,
Essential DSP Implementation Techniques for Xilinx
FPGAs, Designing for Performance.

Publications

- Rizhenko I. N., Andreev A. S., Leonova A.V., Komarov A. A., Realization of the reuse principle for the frequency resource on the basis of universal platform satellite modem, communication, Systems and navigation: abstracts, “Radio”, 2015 -355 pp.
- Leonova A.V., Titovskya T.S. Komarov A.A., An approach to hardware design flow based on functional language Pifagor, Applied and Fundamental Studies: Proceedings of the 5th International Academic Conference. April 29–30, 2014, St. Louis, Missouri, USA.
- Neponyashii O. V., Titovskya T. S., Leonova A.V., Komarov A. A., Formal verification in the design of ultralarge scale integrated circuits, Vestnik of Krasnoyarsk state agrarian University, Krasnoyarsk: publishing house of Krasgau, 2014, № 4.

Hobbies

Hardware development

Listening music

Mountain tracking